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U.S. UTILITY Patent Application

APPL NUM 10037039	FILING DATE 12/21/2001	CLASS <del>281</del> 41	SUBCLASS 155	GAU <del>281</del> 281	EXAMINER L. Mai
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\*\*APPLICANTS: Breinlinger Richard;

\*\*CONTINUING DATA VERIFIED: NO LM

\*\* FOREIGN APPLICATIONS VERIFIED: NO LM

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials LM		ATTORNEY DOCKET NO SAA-83
TITLE : Low cost multi-range input circuit for industrial analog input modules <i>LM</i> <small>U.S. DEPT. OF COMM. (PAT. &amp; TM.) PTO-436L (Rev. 12-94)</small>		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.6
Assistant Examiner  Primary Examiner  PREPARED FOR ISSUE		DRAWING Sheets Drwg. Figs. Drwg. Print Fig. 3	
ISSUE FEE Amount Due Date Paid  <input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner  WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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